

**EXPEDITED PROCEDURE – EXAMINING GROUP 2186**

S/N 10/643,585

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant:	Steven L. Scott	Examiner:	Sheng Jen Tsai
Serial No.:	10/643,585	Group Art Unit:	2186
Filed:	August 18, 2003	Docket No.:	1376.700US1
Title:	LATENCY TOLERANT DISTRIBUTED SHARED MEMORY MULTIPROCESSOR COMPUTER		

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**AMENDMENT & RESPONSE UNDER 37 C.F.R. 1.116**

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

*please do NOT enter.*

*Thanks.*

*ST 8/4/2006*

In response to the Final Office Action mailed April 14, 2006, please amend the application as follows.

This response is accompanied by a Petition, as well as the appropriate fee, to obtain a one-month extension of the period for responding to the Office action, thereby moving the deadline for response from July 14, 2006 to August 14, 2006.